

Query/Command : prt max legalall

/// PLUSPAT - ©QUESTEL-ORBIT

PN - **US5931938** A 19990803 [**US5931938**]
TI - (A) Multiprocessor computer having configurable hardware system domains
PA - (A) SUN MICROSYSTEMS INC (US)
IN - (A) DROGICHEN DANIEL P (US); MCCROCKLIN ANDREW J (US); ANESHANSLEY NICHOLAS E (US)
AP - US76393496 19961212 [1996US-0763934]
PR - US76393496 19961212 [1996US-0763934]
IC - (A) G06F-011/00 G06F-015/77
EC - G06F-015/173N4S
PCL - ORIGINAL (O) : 712015000; CROSS-REFERENCE (X) : 712023000
DT - Corresponding document
CT - US5649106; US5680634; US5689661; US5710938; US5784802
STG - (A) United States patent
AB - Global address and data routers interconnect individual system units each having its own processors, memory, and I/O. A domain filter coupled to the routers dynamically defines groups of system units as domains and clusters of domains which have both software and hardware isolation from each other. Clusters can share dynamically definable ranges of memory with each other. The domain filter has software-loadable registers on the system units and in the global routers to set the parameters of the domains and clusters. The registers label individual inter-system transactions on the routers as invalid for system units not in the same domain or cluster as the originating unit.

/// LGST - ©LEGSTAT

PN - US 5931938 [US5931938]
AP - US 763934/96 19961212 [1996US-0763934]
DT - US-P
ACT - 19961212 US/AE-A
APPLICATION DATA (PATENT)
US 763934/96 19961212 [1996US-0763934]

19990803 US/A
PATENT

20020917 US/RF
REISSUE APPLICATION FILED
20010801
UP - 2002-38

/// CRXX - ©CLAIMS/RRX

PN - 5,931,938 A 19990803 [US5931938]
PA - Sun Microsystems Inc
ACT - 20010801 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20020917
REISSUE REQUEST NUMBER: 09/920433

EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2671

Reissue Patent Number:

/// PAST - ©Thomson Derwent

AN - 200238-001670

PN - 5931938 A [US5931938]

OG - 2002-09-17

ACT - REISSUE APPLICATION FILED

August 3, 1999

Multiprocessor computer having configurable hardware system
domains

APPL-NO: 763934 (08)

FILED-DATE: December 12, 1996

GRANTED-DATE: August 3, 1999

CORE TERMS: domain, cluster, memory, register, computer, router, filter,
global, hardware, processor ...

ENGLISH-ABST:

Global address and data routers interconnect individual system units each having its own processors, memory, and I/O. A domain filter coupled to the routers dynamically defines groups of system units as domains and clusters of domains which have both software and hardware isolation from each other. Clusters can share dynamically definable ranges of memory with each other. The domain filter has software-loadable registers on the system units and in the global routers to set the parameters of the domains and clusters. The registers label individual inter-system transactions on the routers as invalid for system units not in the same domain or cluster as the originating unit.

LEXIS-NEXIS
Library: PATENT
File: ALL

5,931,938 OR 5931938

LEXIS-NEXIS
Library: PATENT
File: CASES

Your search request has found no CASES.

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What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

5,931,938 OR 5931938

LEXIS-NEXIS
Library: PATENT
File: JNLS

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5,931,938 OR 5931938

LEXIS-NEXIS
Library: NEWS
File: CURNWS

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For further explanation, press the H key (for HELP) and then the ENTER key.

?s pn=us 5931938
S3 1 PN=US 5931938
?t 3/39/1

3/39/1
DIALOG(R) File 345:Inpadoc/Fam.& Legal Stat
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14379639

Basic Patent (No,Kind,Date): EP 848327 A2 19980617 <No. of Patents: 004>

Patent Family:

Patent No	Kind	Date	Applic No	Kind	Date	
EP 848327	A2	19980617	EP 97310070	A	19971212	(BASIC)
EP 848327	A3	20020123	EP 97310070	A	19971212	
JP 10228458	A2	19980825	JP 97342920	A	19971212	
US 5931938	A	19990803	US 763934	A	19961212	

Priority Data (No,Kind,Date):

US 763934 A 19961212

PATENT FAMILY:

EUROPEAN PATENT OFFICE (EP)

Patent (No,Kind,Date): EP 848327 A2 19980617

MULTIPROCESSOR COMPUTER HAVING CONFIGURABLE HARDWARE SYSTEM DOMAINS
(English; French; German)

Patent Assignee: SUN MICROSYSTEMS INC (US)

Author (Inventor): DROGICHEN DANIEL P (US); MCCROCKLIN ANDREW J (US)
; ANESHANSLEY NICHOLAS E (US)

Priority (No,Kind,Date): US 763934 A 19961212

Applic (No,Kind,Date): EP 97310070 A 19971212

Designated States: (National) DE; FR; GB; IT; NL; SE

IPC: * G06F-009/46

Language of Document: English

Patent (No,Kind,Date): EP 848327 A3 20020123

MULTIPROCESSOR COMPUTER HAVING CONFIGURABLE HARDWARE SYSTEM DOMAINS
(English; French; German)

Patent Assignee: SUN MICROSYSTEMS INC (US)

Author (Inventor): DROGICHEN DANIEL P (US); MCCROCKLIN ANDREW J (US)
; ANESHANSLEY NICHOLAS E (US)

Priority (No,Kind,Date): US 763934 A 19961212

Applic (No,Kind,Date): EP 97310070 A 19971212

Designated States: (National) AT; BE; CH; DE; DK; ES; FI; FR; GB; GR;
IE; IT; LI; LU; MC; NL; PT; SE

IPC: * G06F-009/46; G06F-015/173

Language of Document: English

EUROPEAN PATENT OFFICE (EP)

Legal Status (No,Type,Date,Code,Text):

EP 848327	P	19961212	EP AA	PRIORITY (PATENT APPLICATION)	(PRIORITAET (PATENTANMELDUNG))
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EP 848327	P	19971212	EP AE	US 763934 A 19961212	EP-APPLICATION
					(EUROPAEISCHE ANMELDUNG)

EP 848327	P	19980617	EP AK	EP 97310070 A 19971212	DESIGNATED CONTRACTING STATES IN AN APPLICATION WITHOUT SEARCH REPORT: (IN EINER ANMELDUNG OHNE RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN)
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EP 848327	P	19980617	EP AX	DE FR GB IT NL SE	ERSTRECKUNG DES EUROPAEISCHEN PATENTS AUF (ZAHLUNG VON BENENNUNGSGEBUEHREN)
				AL;LT;LV;MK;RO;SI	

EP 848327	P	19980617	EP A2	PUBLICATION OF APPLICATION WITHOUT SEARCH REPORT (VEROEFFENTLICHUNG DER ANMELDUNG OHNE RECHERCHENBERICHT)	
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EP 848327	P	20020123	EP AK	DESIGNATED CONTRACTING	
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STATES IN A SEARCH REPORT: (IN EINEM
RECHERCHENBERICHT BENANNTE VERTRAGSSTAATEN)

AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC
NL PT SE
EP 848327 P 20020123 EP AX EXTENSION OF THE EUROPEAN
PATENT TO (ERSTRECKUNG DES EUROPÄISCHEN
PATENTS AUF)
AL;LT;LV;MK;RO;SI
EP 848327 P 20020123 EP A3 SEPARATE PUBLICATION OF THE
SEARCH REPORT (ART. 93) (GESONDERTE
VERÖFFENTLICHUNG DES RECHERCHENBERICHTS
(ART. 93))
EP 848327 P 20020123 EP RIC1 CLASSIFICATION (CORRECTION)
(KLASSIFIKATION (KORR.))
7G 06F 9/46 A, 7G 06F 15/173 B
EP 848327 P 20020814 EP 17P REQUEST FOR EXAMINATION
FILED (PRÜFUNGSANTRAG GESTELLT)
20020527
EP 848327 P 20021009 EP AKX PAYMENT OF DESIGNATION FEES
(ZAHLUNG VON BENENNUNGSGEBÜHREN)
DE FR GB IT NL SE

JAPAN (JP)

Patent (No,Kind,Date): JP 10228458 A2 19980825
MULTIPROCESSOR COMPUTER HAVING CONFIGURABLE HARDWARE SYSTEM DOMAIN
(English)
Patent Assignee: SUN MICROSYSTEMS INC
Author (Inventor): DANIEL P DOROGICHEN; ANDREW J MCCLAUCLIN; NICHOLAS
E ANESHANZUREE
Priority (No,Kind,Date): US 763934 A 19961212
Applic (No,Kind,Date): JP 97342920 A 19971212
IPC: * G06F-015/177; G06F-015/16
Language of Document: Japanese

UNITED STATES OF AMERICA (US)

Patent (No,Kind,Date): US 5931938 A 19990803
MULTIPROCESSOR COMPUTER HAVING CONFIGURABLE HARDWARE SYSTEM DOMAINS
(English)
Patent Assignee: SUN MICROSYSTEMS INC (US)
Author (Inventor): DOROGICHEN DANIEL P (US); MCCROCKLIN ANDREW J (US)
; ANESHANSLEY NICHOLAS E (US)
Priority (No,Kind,Date): US 763934 A 19961212
Applic (No,Kind,Date): US 763934 A 19961212
National Class: * 712015000; 712023000
IPC: * G06F-015/77; G06F-011/00
Language of Document: English

UNITED STATES OF AMERICA (US)

Legal Status (No,Type,Date,Code,Text):
US 5931938 P 19961212 US AE APPLICATION DATA (PATENT)
(APPL. DATA (PATENT))
US 763934 A 19961212
US 5931938 P 19990803 US A PATENT
US 5931938 P 20020917 US RF REISSUE APPLICATION FILED
(REISSUE APPL. FILED)
20010801